

5TH SEMESTER E&TC ENGG. TIME TABLE-2021

DAY	10:00 - 10:45	10:45 - 11:30	11:30 - 12:15	12:15 - 1:00	1:00 - 2:00	2:00 - 4:15
MON	E&MS	AC&DC	VLSI&ES	PE&PLC	L	VLSI&EMBEDDED System Lab
TUE	E&MS	AC&DC	PE&PLC	VLSI&ES	A	Analog & Digital communication Lab
WED	E&MS	AC&DC	VLSI&ES	WP&BBCE	U	Power Electronics Lab
THUR	E&MS	AC&DC	VLSI&ES	WP&BBCE	N	WP&BBCE Lab
FRI	E&MS	VLSI&ES	PE&PLC	WP&BBCE	C	Project Phase-1
SAT	AC&DC	WP&BBCE	PE&PLC	WP&BBCE	H	XXXXXX

TH-1:E&MS:Tapas Ku.Giri	Pr-1:AC&DC Lab:SK MINAZ KADERI
TH-2:Deboshree Mohanty	Pr-2:VLSI&ES Lab:Rachana Nayak
TH-3:SK MINAZ KADERI	Pr-3:Sameer Kumar Patra
TH-4:SAMEER KUMAR PATRA	Pr-4:PE Lab:N.Sahoo/Atul Ku. Bhanja
TH-5:ATUL KUMAR BHANJA	Project Phase-1:All Staff


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 SSB RI