

**SSB REGIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY, CHITRADA, MAYURBHANJ**

**LESSION PLAN**

BRANCH:ELECTRICAL ENGG		SESSION:2022-2023		SEMESTER		5TH	
NAME OF FACULTY : JABAMANI SIRKA				SUBJECT		DIGITAL ELECTRONICS & MICROPROCESSOR	
NO OF CLASSES/WEEK GIVEN AS PER SYLLABUS =05			DATE OF SEMESTER STARTING		05/06/2022		DATE OF SEMESTER CLOSING
NO OF CLASSES/WEEK ALLOTTED AS PER TIME TABLE =05			TOTAL NOS. OF WORKING DAYS AS PER SCTE&VT:				
Sl No.	Chapter	Name of Topic	As per Syllabus No. of classes allotted	As per plan No. of classes required to complete	Detail Content of this chapter		
1	1	Basics Of Digital Electronics	15	15	1.1 Binary, Octal, Hexadecimal number systems and compare with Decimal system 1.2 Binary addition, subtraction, Multiplication and Division. 1.3 1's complement and 2's complement numbers for a binary number 1.4 Subtraction of binary numbers in 2's complement method. 1.5 Use of weighted and Un-weighted codes & write Binary equivalent		
2	2	Combinational Logic Circuits	15	16	2.1 Give the concept of combinational logic circuits. 2.2 Half adder circuit and verify its functionality using truth table. 2.3 Realize a Half-adder using NAND gates only and NOR gates only. 2.4 Full adder circuit and explain its operation with truth table. 2.5 Realize full-adder using two Half-adders and an OR – gate and write truth table		
3	3	Sequential Logic Circuits	15	16	3.1 Give the idea of Sequential logic circuits. 3.2 State the necessity of clock and give the concept of level clocking and edge triggering, 3.3 Clocked SR flip flop with preset and clear inputs. 3.5 Construct level clocked JK flip flop using S-R flip-flop and explain with truth table		
4	4	8085 Microprocessor	20	22	4.1 Introduction to Microprocessors, Microcomputers 4.2 Architecture of Intel 8085A Microprocessor and description of each block. 4.3 Pin diagram and description. 4.4 Stack, Stack pointer & stack top 4.5 Interrupts		
5	5	Interfacing And Support Chips	10	10	5.1 Basic Interfacing Concepts, Memory mapping & I/O mapping 5.2 Functional block diagram and description of each block of Programmable peripheral interface Intel 8255 , 5.3 Application using 8255: Seven segment LED display, Square wave generator, Traffic light		